

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#6 Pre Amt
10-31-01
InitialsIn re divisional patent application of 09/387,268 filed on August 31, 1999

Yamauchi, K.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: SEMICONDUCTOR LASER DIODE CHIP AND ITS POSITIONING AND
MOUNTING METHODAssistant Commissioner of Patents
Washington, D.C. 20231PRELIMINARY AMENDMENT

Sir:

Prior to examination on the merits and calculation of the filing fee, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Page 1, before line 3, insert --The present Application is a Divisional Application of

A1 No. 6,294,732 B1
U.S. Patent Application No. 09/387,268, filed on August 31, 1999.--

IN THE CLAIMS:

Please cancel claims 10-20 without prejudice or disclaimer.

REMARKS

Claims 10-20 have been canceled to allow prosecution of claims 1-9. No new matter has been added.

With respect to the prior co-pending U.S. application Serial No. 09/387,268, Applicant hereby incorporates the claim of priority from Japanese Application 249331/1998